WEST Search History

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DATE: Monday, March 14, 2005

Hide?	Set Name	Query	Hit Count
		DB=PGPB,USPT; PLUR=NO; OP=ADJ	
☐	L64	158 and 153	0
☐	L63	158 and 154	5
☐	L62	158 and 137	0
☐	L61	154 and 156	7
☐	L60	156 and 158	5
☐	L59	153 and 154	11
☐	L58	712/248.ccls.	172
☐	L57	712/227.ccls.	526
☐	L56	712/229.ccls.	269
☐	L55	712/32.ccls.	355
☐	L54	712/37.ccls.	152
☐	L53	712/39.ccls.	70
☐	L52	atmel.asn. and spi	7
☐	L51	L6 and L50	1
☐	L50	(overwrit$4 or over-writ$4 or eras$3) same L49	39
☐	L49	test$3 same calibrat$3 same program$5	3597
☐	L48	(overwrit$4 or over-writ$4 or eras$3) same L47	1
☐	L47	test$3 same calibrat$3 same microcontroller$1	286
☐	L46	(overwrit$4 or over-writ$4) same L27	0
☐	L45	(overwrit$4 or over-writ$4) same (L40 or L41)	1
☐	L44	wright.in. and wick.in.	0
☐	L43	L40 and L41	0
☐	L42	L40 and L24L24	0
☐	L41	load$3 with calibration program$1	39
☐	L40	load$3 with test program$1	613
☐	L39	calibration program$3 with eras$3	1
☐	L38	test program$3 with eras$3	53
☐	L37	5428770.uref.	11
☐	L36	microcontroller$1 and L35	49
☐	L35	L33 and L34	814
☐	L34	714/$.ccls.	30469

☐	L33	702/$.ccls.	27073
☐	L32	dual purpose interface	5
☐	L31	test program$1 same calibration program$1	10
☐	L30	L29 and microcontroller$1	3
☐	L29	(702/119).ccls. and (702/120).ccls.	25
☐	L28	L27 same (manufactur$3 or fabricat$3)	7
☐	L27	test$3 same calibrat$3 same microcontroller$1 same mode	60
☐	L26	test$3.clm. and L18	35
☐	L25	test$3.clm. and L22	5
☐	L24	test$3.clm. and chip.clm. and L22	0
☐	L23	test$3.clm. and manufactur$3.clm. and L22	0
☐	L22	treat.xa.	66
☐	L21	test$3.clm. and manufactur$3.clm. and L18	1
☐	L20	test$3.clm. and chip.clm. and L18	2
☐	L19	test$3.clm. and chip.clm. L18	0
☐	L18	treat.xp.	605
☐	L17	manufactur$3 same L8	51
☐	L16	(L11 or L13) and usb	4
☐	L15	L7 and L11	25
☐	L14	L7 and L13	8
☐	L13	5596734.uref.	50
☐	L12	5596734.pn.	1
☐	L11	5495593.uref.	31
☐	L10	5495593.pn.	1
☐	L9	test$3 same L8	26
☐	L8	L6 with L7	457
☐	L7	re-program$8 or reprogram$8	25519
☐	L6	microcontroller$1 or micro-controller$1	47098

DB=USPT; PLUR=NO; OP=ADJ

☐	L5	5467286.pn.	1

DB=EPAB; PLUR=NO; OP=ADJ

☐	L4	5467286.pn.	0
☐	L3	embedded.ti. and sun.in. and processor.ti.	1

DB=DWPI; PLUR=NO; OP=ADJ

☐	L2	embedded.ti. and sun.in. and processor.ti.	0
☐	L1	embedded.ti. and sun.in.	36

END OF SEARCH HISTORY